

METHOD AND SYSTEM FOR SETTING SPECIAL PIPE LINE TEMPORARILY IN PROCESSOR DEVICE

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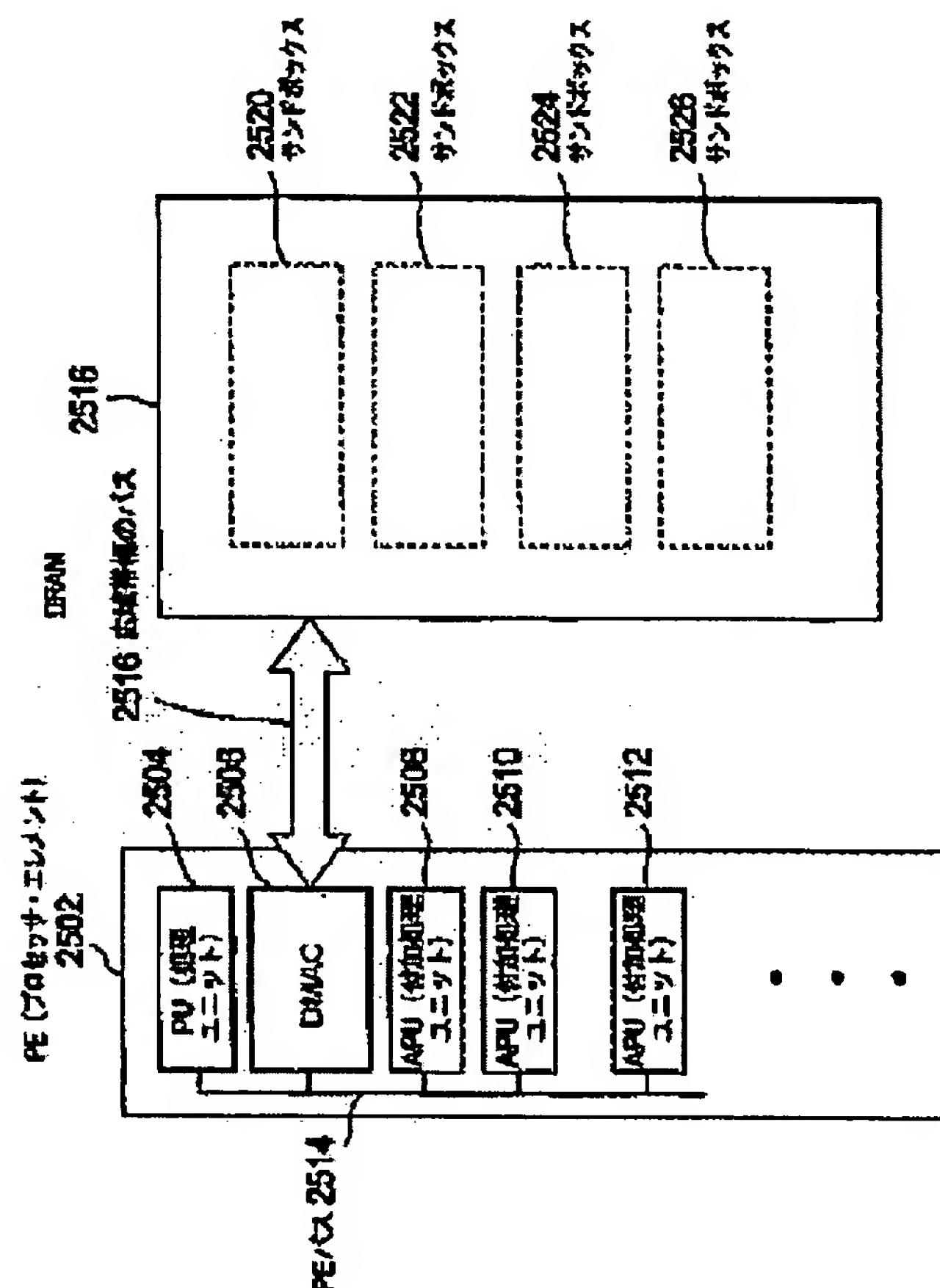


WO02077838 (A1)
 EP1370961 (A1)
 US6809734 (B2)
 US2002135582 (A1)
 JP2004046861 (A)

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Abstract of JP2002358289

PROBLEM TO BE SOLVED: To provide a computer architecture for a high-speed processing and a programming model via a broadband network. **SOLUTION:** The architecture uses a uniform modular structure, a common computing module and a uniform software cell. The common module includes a control device, more than one processing unit, more than one local memory causing the processing units to process a program, a direct memory access, a controller and a shared main memory. A synchronizing system and method causing the processing units to execute adjusted reading and writing of data from the shared main memory are provided.



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